

Fig. 3

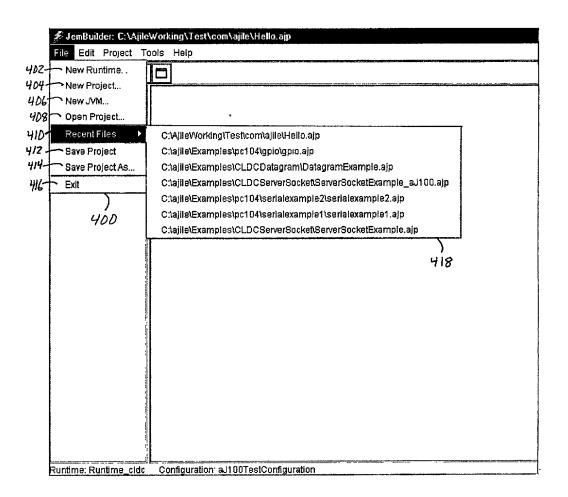


Fig. 4

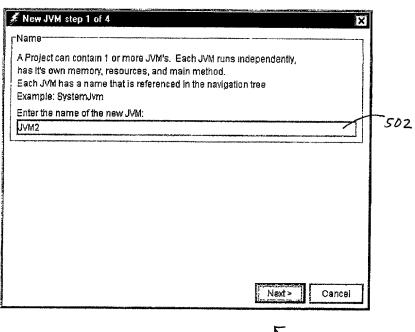
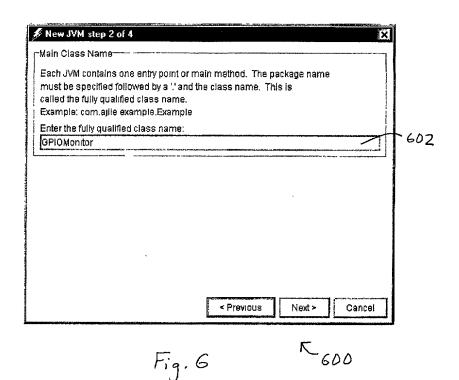
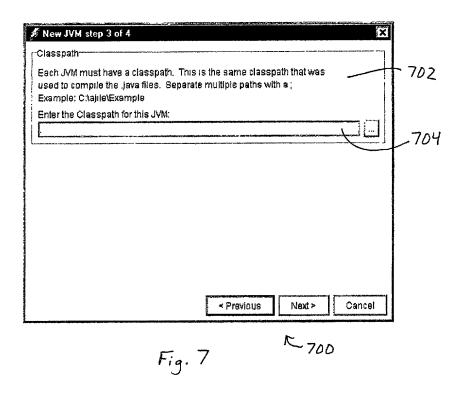
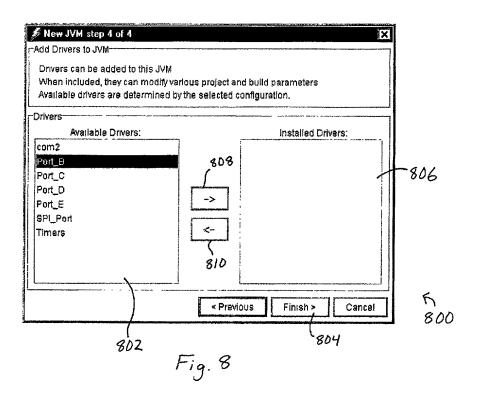


Fig. 5







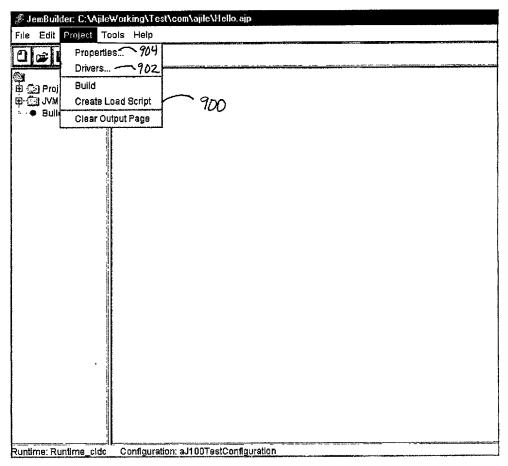
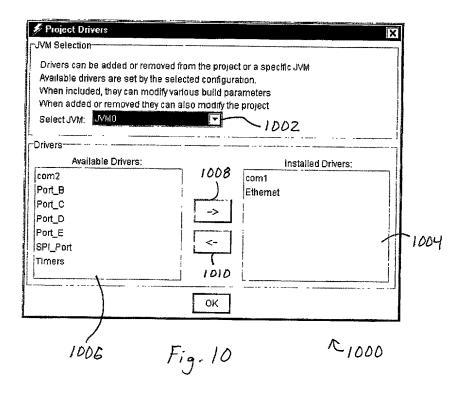
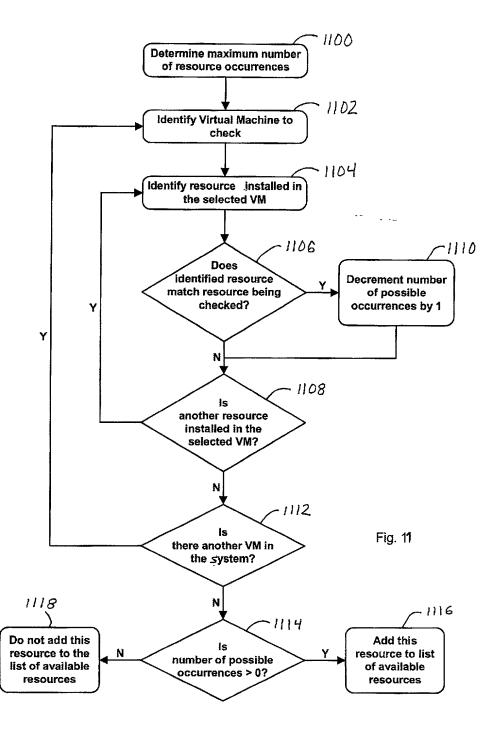
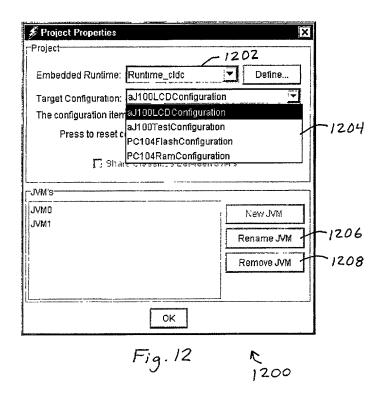


Fig.9







💰 JemBuilder: C:\local\Se	rverSocket_and_GPIO.ajp
File Edit Project Tools	Help
Project  Output Files  PLL Setup  PIN Setup1  Pin Setup2  Chip Selects  JVM0  Main  Classpath  Memory  Propertles  Listing  Optimizations  aJ100 Interrupts  Invokes  Traps  Patch Classes  More Classes  Memory  Propertles  Listing  Optimizations  aJ100 Interrupts  Invokes  Traps  Patch Classes  Memory  Properties  Listing  Optimizations  aJ100 Interrupts  Invokes  Traps  AJ100 Interrupts  Invokes  Traps  AJ100 Interrupts  Invokes  Traps  AJ100 Interrupts  Invokes  Traps  Bulld Output	Physical Memory  RAM Start Address: Dx0
Runtime: Runtime_cidc C	onfiguration: aJ100LCDConfiguration

Fig. 13

🕏 JemBuilder: C:\local\Se	rverSocket_and_GPIO.ajp
File Edit Project Tools	Help
Project  Output Files  Memory  Pin Setup1  Pin Setup2  Chip Selects  JVM0  Main  Classpath  Memory  Properties  Listing  Optimizations  invokes  Traps  Patch Classes  More Classes  Memory  Properties  Listing  Optimizations  Al 100 Interrupts  Invokes  Traps  Patch Classes  More Classes  Memory  Properties  Listing  Optimizations  Al 100 Interrupts  Invokes  Traps  Properties  Build Output	Phased Locked Loop Enable    Phased Locked Loop Enable   Phased Locked Loop Enable   Phased Locked Loop Enable   Phased Locked Loop Enable   Phased Locked Loop Enable   Phased Locked Loop Enable   Phased Locked Loop Enable   Ithe lock timeout can be used to bypass the PLL for a number of clock cycles, until the PLL stabilizes. The input clock will be used for the specified number of input clocks, then a switch is made to the PLL output. If the lock timeout is disabled, the PLL output will be used all of the time   3,670,016
Runtime: Runtime_cidc Co	miguration: aJ1 UULCDConfiguration
1402	Fig. 14

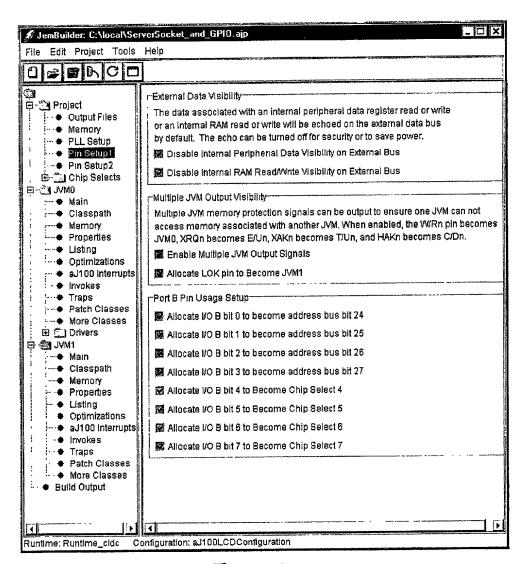


Fig. 15

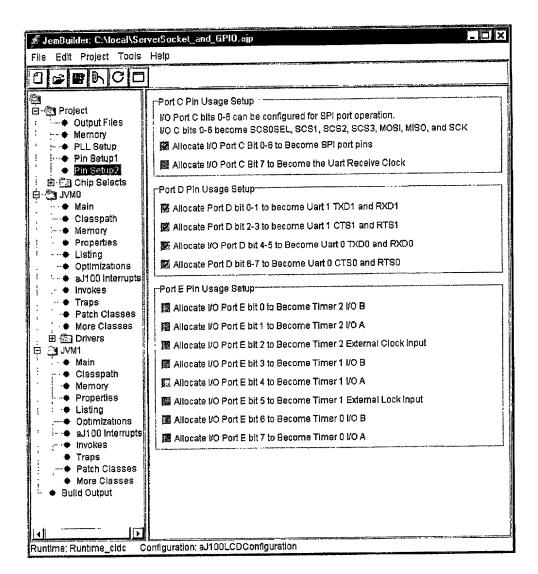


Fig. 16

JemBuilder: C:\local\ServerSocket_and_GP10.ajp     □□     □□
File Edit Project Tools Help
Address Range This Chip Select Includes 4MB from 0x0 to 0x3FFFF  This Chip Select Includes 4MB from 0x0 to 0x3FFFFF  This Chip Select Includes 4MB from 0x0 to 0x3FFFFF  Bus Width The external data bus width is configurable from 8 to 32 bits 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable from 8 to 32 bits Select the Bus Width The external data bus width is configurable form 8 to 32 bits Select the Bus Width The external data bus width is configurable form 8 to 32 bits Select the Bus Width The external data bus width is configurable form 8 to 32 bits Select the Bus Width The external data
Runtime: Runtime_cldc

Fig. 17 1700

# JemBuilder: C:\local\S	erverSocket_and_GP10.ajp
File Edit Project Took	: Help
OBBRCC	
Project Output Files Memory PLL Setup Pin Setup1 Pin Setup2 Chip Selects JVM0 Classpath Memory Properties Listing Optimizations JIOO Interrupts Invokes Traps Patch Classes More Classpath Memory Properties Listing Optimizations JVM1 Main Classpath Memory Properties Listing Optimizations JVM1 Main Classpath Memory Properties Listing Optimizations JIVM1 Main Classpath Memory Properties Listing Optimizations JIOO Interrupts Invokes Traps Patch Classes More Classes More Classes Build Output	Application Main Method  Fully Qualified Class Name of Main Method eg(com.ajile.example.MainClass):  Examples.ServerSocket.ServerSocketExample  Fully qualified name of Main Method (default. main([Ljava/lang/String;)V):  Static Initialization  Fully Qualified Class Name to be Initialized First:  Additional Methods  Method Called When JVM is Idle, Must have signature ()V:  Method Called When Uncaught Exception:  Must have method signature (Ljava/lang/Throwable;)V

Fig. 18

Project  Output Files  Memory  PIL Setup  Pin Setup1  Pin Setup2  Chip Selects  JVM0  Main  Memory  Properties  Listing  Optimizations  All Onterrupts  Invokes  Traps  Patch Classes  Mere Classes  Memory  Memory  Properties  Listing  Optimizations  All Onterrupts  Invokes  Traps  Patch Classes  More Classes  Memory  Properties  Listing  Optimizations  All Onterrupts  Invokes  Traps  Patch Classes  Memory  Properties  Listing  Optimizations  All Onterrupts  Invokes  Traps  Patch Classes  Memory  Properties  Listing  Optimizations  All Onterrupts  Invokes  Traps  Patch Classes  More Classes  More Classes  More Classes	Application Classpath:  Classpath:  C:\ajile  Libraries:  Available Libraries:  ->  Available Available Libraries:

Fig. 19

# JemBuilder: C:\local\Se File Edit Project Tools	· · · · · · · · · · · · · · · · · · ·	<u> </u>
		**************************************
Project Output Files Memory PLL Setup Pin Setup1 Pin Setup2 Chip Selects Divers Properties Listing Optimizations AJ100 Interrupts Invokes Traps Patch Classes Drivers Drivers Drivers Drivers Listing Optimizations AJ100 Interrupts Invokes Traps Patch Classes Invokes Traps Properties Invokes Traps Patch Classes Invokes Traps Properties Invokes Traps Properties Invokes Traps Properties Invokes Traps Properties Usting Optimizations AJ100 Interrupts Invokes Traps Patch Classes Build Output	UVM Placement  IVI Locate JVM In next available memory  RAM (Data) Start Address:  RAM (Data) Size (bytes):  ROM (Code) Start Address:  ROM (Code) Size (bytes):  Memory Allocations (bytes)  Initial Heap Size:  Ox4000  Exec Heap Size:  Ox6000  Main Thread Stack Size:  Ox400  Idle Thread Stack Size:  Ox1000  Executive Stack Size:  Ox1000  Abort Stack Size:  Ox1000  Thread Control Block Size:  Oxb0	Default
Runtime: Runtime cldc C	onfiguration: aJ100LCDConfiguration	

Fig. 20

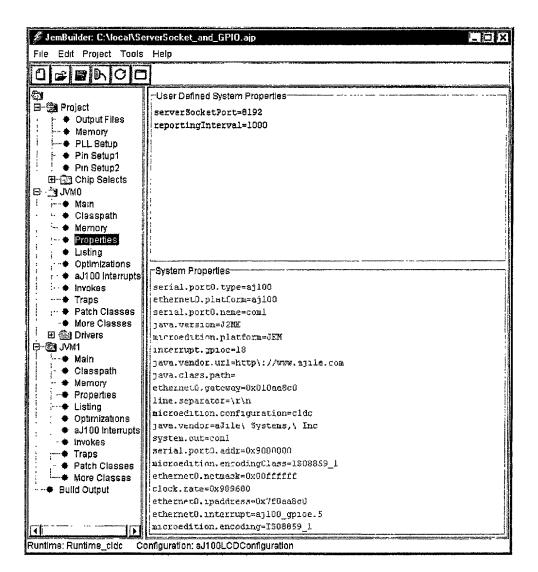


Fig. 21

Fig. 22

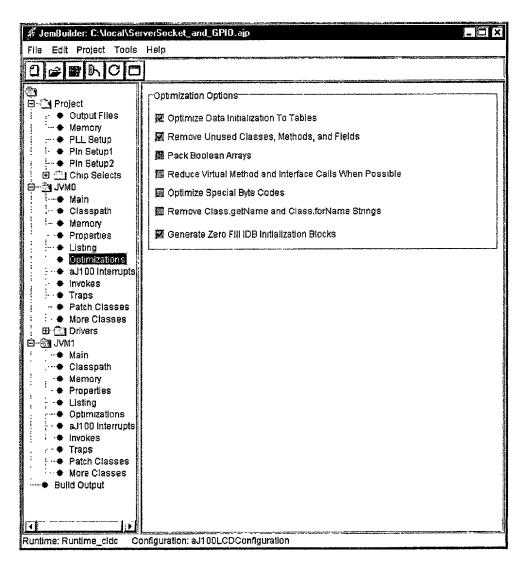


Fig. 23

🖋 JemBuilder: C:\local\Se		×
File Edit Project Tools		
Project Output Files Memory PIL Setup Pin Setup1 Pin Setup2 Chip Selects Chip Selects Classpath Memory Properties Listing Optimizations Invokes Traps Patch Classes More Classes Drivers Drivers Listing Optimizations Invokes Traps Patch Classes Memory Properties Invokes Traps Patch Classes More Classes Listing Optimizations JUM1 Invokes Traps Patch Classes Invokes Traps Patch Classes Upperties Listing Optimizations JUM1 Invokes Traps Patch Classes Invokes Traps Patch Classes More Classes More Classes Build Output	Transfer Error XERR   IRQ 0:	(A)
Runtime: Runtime_cldc Cr	com.ajile.drivers.irq.interruptController.gpioEinterrupt()V \ 18 \ 18 \ 100LCDConfiguration	
2400	2406	J

Fig. 24

	rverSocket_and_GPIO.ajp
File Edit Project Tools	
Divers  Project  Output Files  Memory  PIL Setup  Pin Setup1  Pin Setup2  Chip Selects  JVM0  Main  Classpath  Memory  Properties  Listing  Optimizations  JI 100 Interrupts  Memory  Properties  Traps  Patch Classes  More Classes  Divers  JVM1  Main  Classpath  Memory  Properties  JVM1  Main  Classpath  Memory  Properties  Listing  Optimizations  JVM1  Main  Classpath  Memory  Properties  Listing  Optimizations  JVM1  Main  Classpath  Memory  Properties  Listing  Optimizations  JVM1  Memory  Properties  Listing  Optimizations  JI 100 Interrupts  Invokes  Traps  Patch Classes  More Classes  More Classes  More Classes	Implicit Invoke Handlers  Fully Qualified Method Name Including Signature eg: com.ajile.Uart.handler()V  Note: Must be a static non-synchronized method multiArray(II)Ljava/lang/Object; Invoke 0: com.ajile.jem.RTS_Memory.invokeMultiArray()Ljava/lang/Object; resolveClassRef(III)V Invoke 1: com.ajile.jem.RTS_DLink.invokeResolveClassRef(II)V resolveInstanceFieldRef(II)V Invoke 2: com.ajile.jem.RTS_DLink.invokeResolveInstanceFieldRef(II)V resolveClassFieldRef(II)V Invoke 3: com.ajile.jem.RTS_DLink.invokeResolveClassFieldRef(II)V resolveInstanceMethodRef(II)V Invoke 4: com.ajile.jem.RTS_DLink.invokeResolveInstanceMethodRef(II)V resolveClassMethodRef(II)V Invoke 5: com.ajile.jem.RTS_DLink.invokeResolveInstanceMethodRef(II)V resolveClassMethodRef(II)V Invoke 6: com.ajile.jem.RTS_DLink.invokeResolveInstanceMethodRef(II)V resolveInterfaceMethodRef(II)V Invoke 6: com.ajile.jem.RTS_DLink.invokeResolveInterfaceMethodRef(II)V stackChunkCopy(II)V Invoke 7: com ajile.jem.RTS_DLink.invokeResolveInterfaceMethodRef(II)V resolveInterfaceMethodRef(II)V Invoke 8: com.ajile.jem.RTS_Exception.invokeThrowID(III)V aThrowID(III)V aThrowID(III)V Invoke 8: com.ajile.jem.RTS_Exception.invokeThrowObject(IILjava/lang/Throwable;)V  2500  2504
Runtime: Runtime cldc C	onfiguration: aJ100LCDConfiguration

Fig. 25

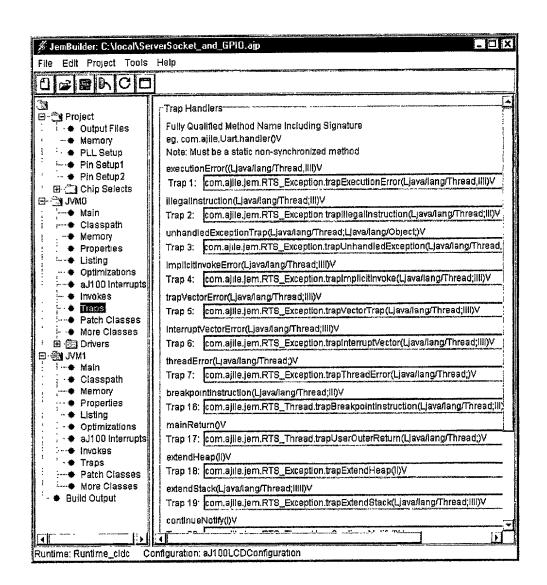


Fig. 26

🖋 JemBuilder: C:\local\Se	erverSocket_and_GP10.ajp
File Edit Project Tools	Help
Project Output Files Memory PLL Setup Pin Setup1 Pin Setup2 Chip Selects JVM0 Main Classpath Memory Properties Listing Optimizations aJ100 Interrupts Invokes Traps Patch Classes Memory Properties Listing Optimizations aJ100 interrupts Invokes Traps Patch Classes Memory Properties Listing Optimizations JVM1 Main Classpath Memory Properties Listing Optimizations JVM1 Main Classpath Memory Properties Listing Optimizations JUM1 Memory Properties Listing Optimizations Jumpations Jumpations Jumpations Memory Properties Listing Optimizations Jumpations Jum	->

Fig. 27

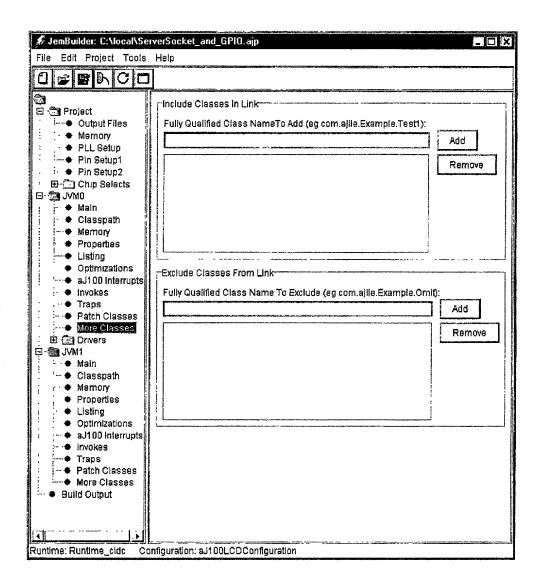


Fig. 28

# JemBuilder: C:\local\ServerSocket_and_GPIO.ajp  □□□  □□□    □□□□
File Edit Project Tools Help
Ethernet Storage Choices  Ethernet IP Information can be stored as system properties, Or a memory location can be specified where the IP information can be found  Prin Setup Pr

Fig. 29

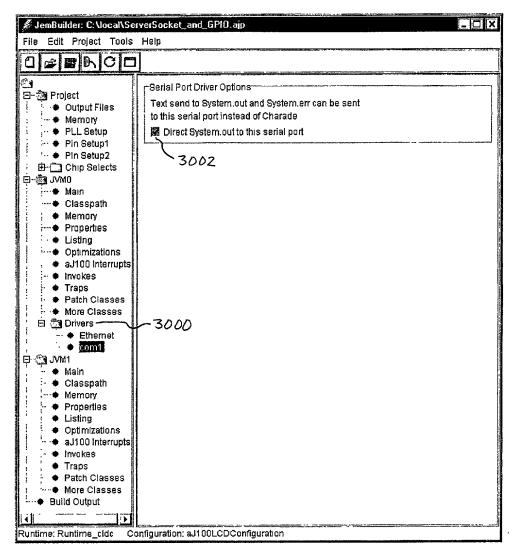


Fig. 30

💰 JemBuilder: C:\local\Se	erverSocket_and_GPIO.ajp	×
File Edit Project Tools	Help	
O S B D C C		
Project  Output Files  Memory  PIL Setup  Pin Setup1  Pin Setup2  Chip Selects  JVM0  Main Classpath Memory Properties Listing Optimizations AJ100 Interrupts Invokes Traps	Fully Qualified Method Name Including Signature eg: com.ajile Uart.handler()V Note: Must be a static non-synchronized method with ()V signature Transfer Error XERR IRQ 0. Power Down Warning PDW IRQ 1: External NMI IRQ 3: Arithmetic Error (JVM aware) IRQ 5: Timer/Counter TCO IRQ 7:	
Patch Classes  More Classes  Ethernet  com1  JVM1  Main  Classpath  Memory  Properties  Custing  Optimizations  Invokes  Traps  Patch Classes  More Classes  Bulld Output	Peripheral Interrupts	
Runtime Runtime_cldc C	onfiguration: aJ100LCDConfiguration	
	3100	/
	Fig. 31	

Page 62 of 62